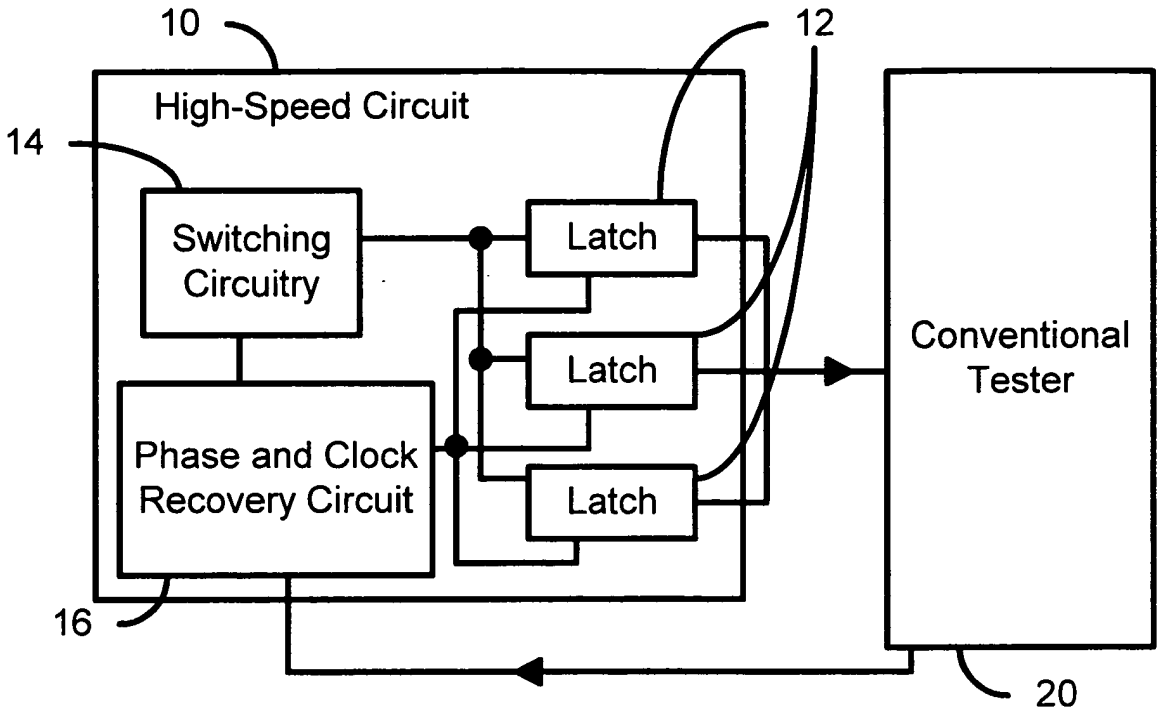

Prior Art

Figure 1

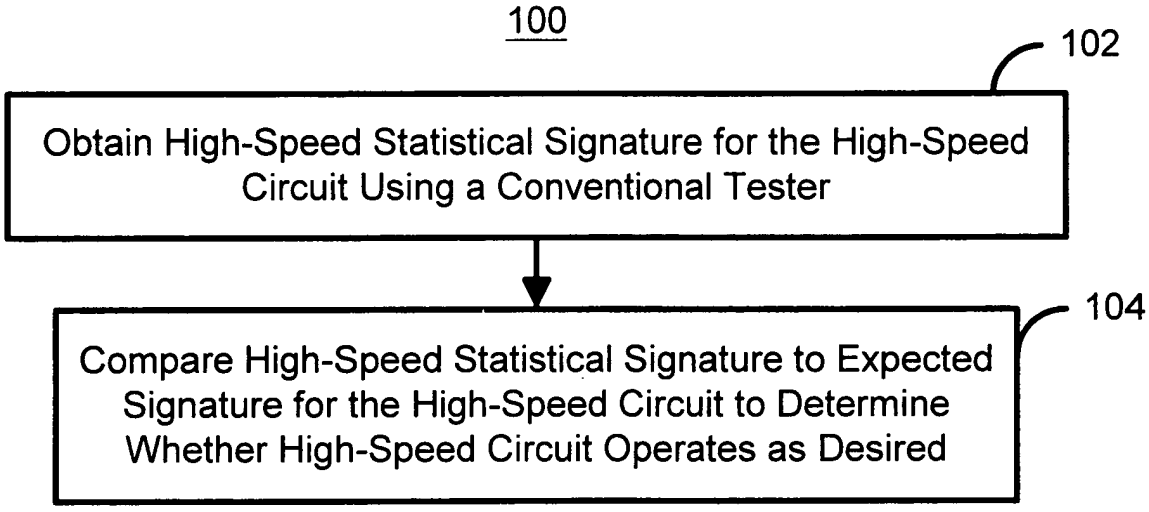
100
102
Obtain High-Speed Statistical Signature for the High-Speed Circuit Using a Conventional Tester
104
Compare High-Speed Statistical Signature to Expected Signature for the High-Speed Circuit to Determine Whether High-Speed Circuit Operates as Desired

Figure 2

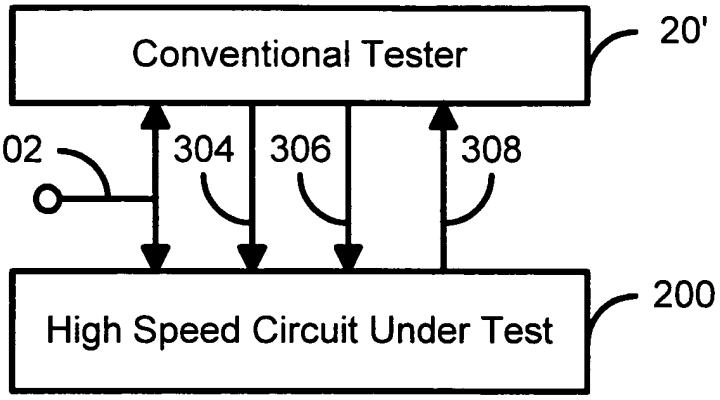

Figure 3A

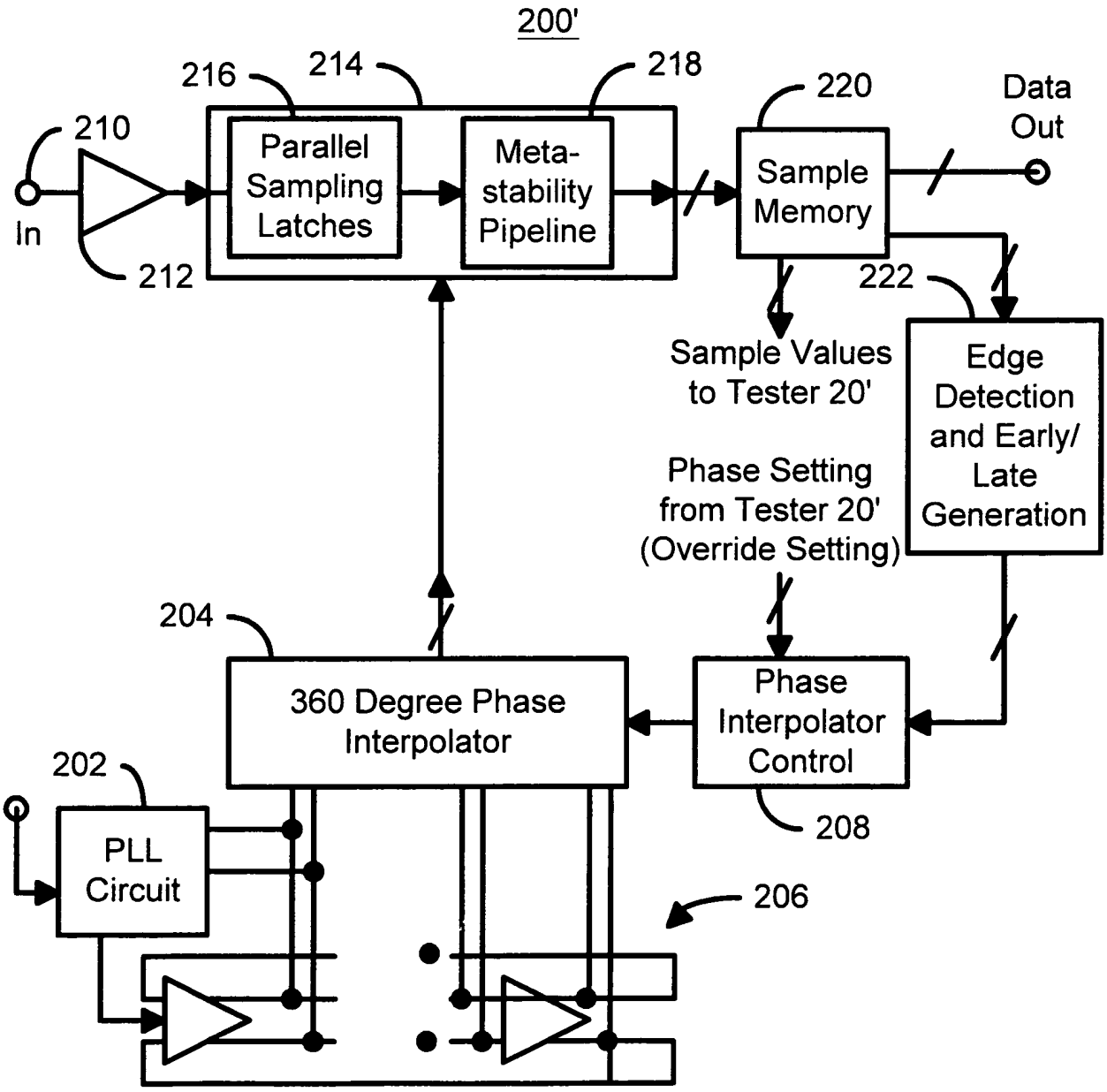

Figure 3B

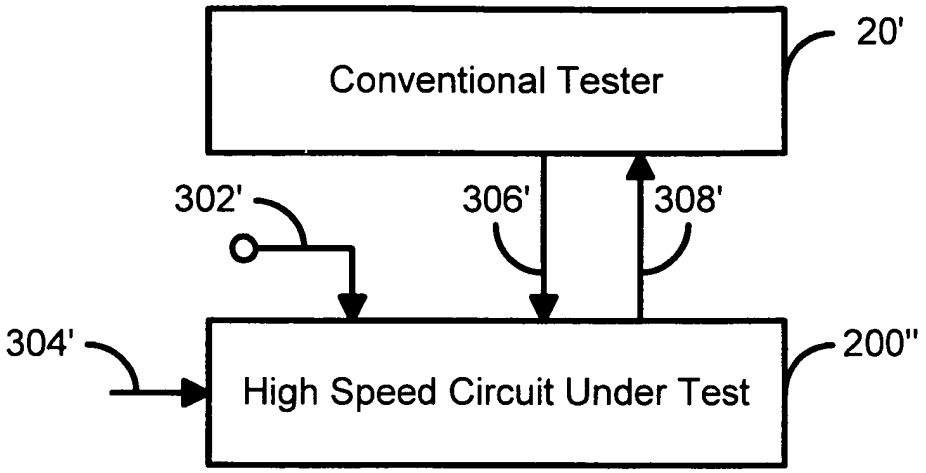

Figure 3C

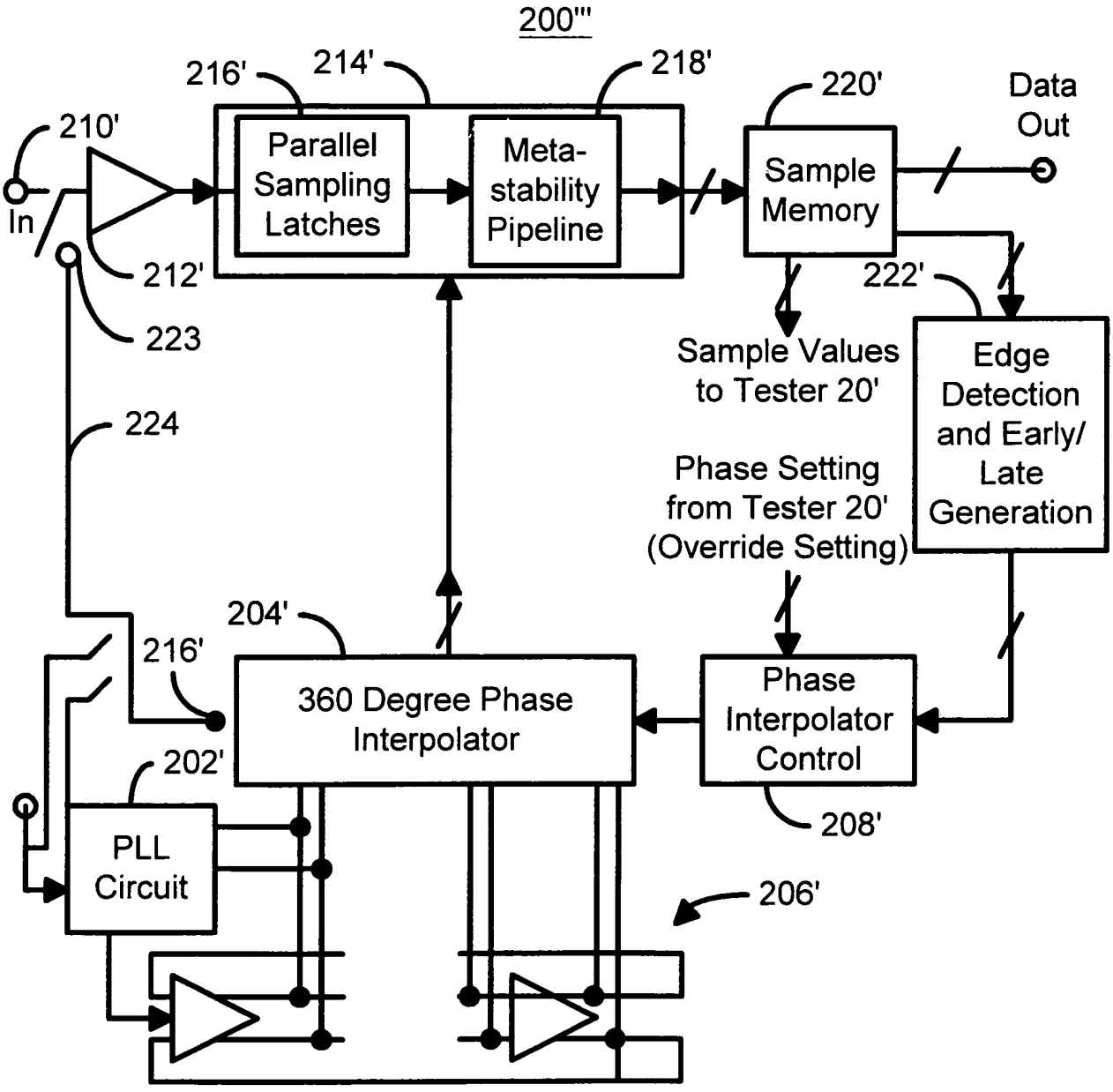

Figure 3D

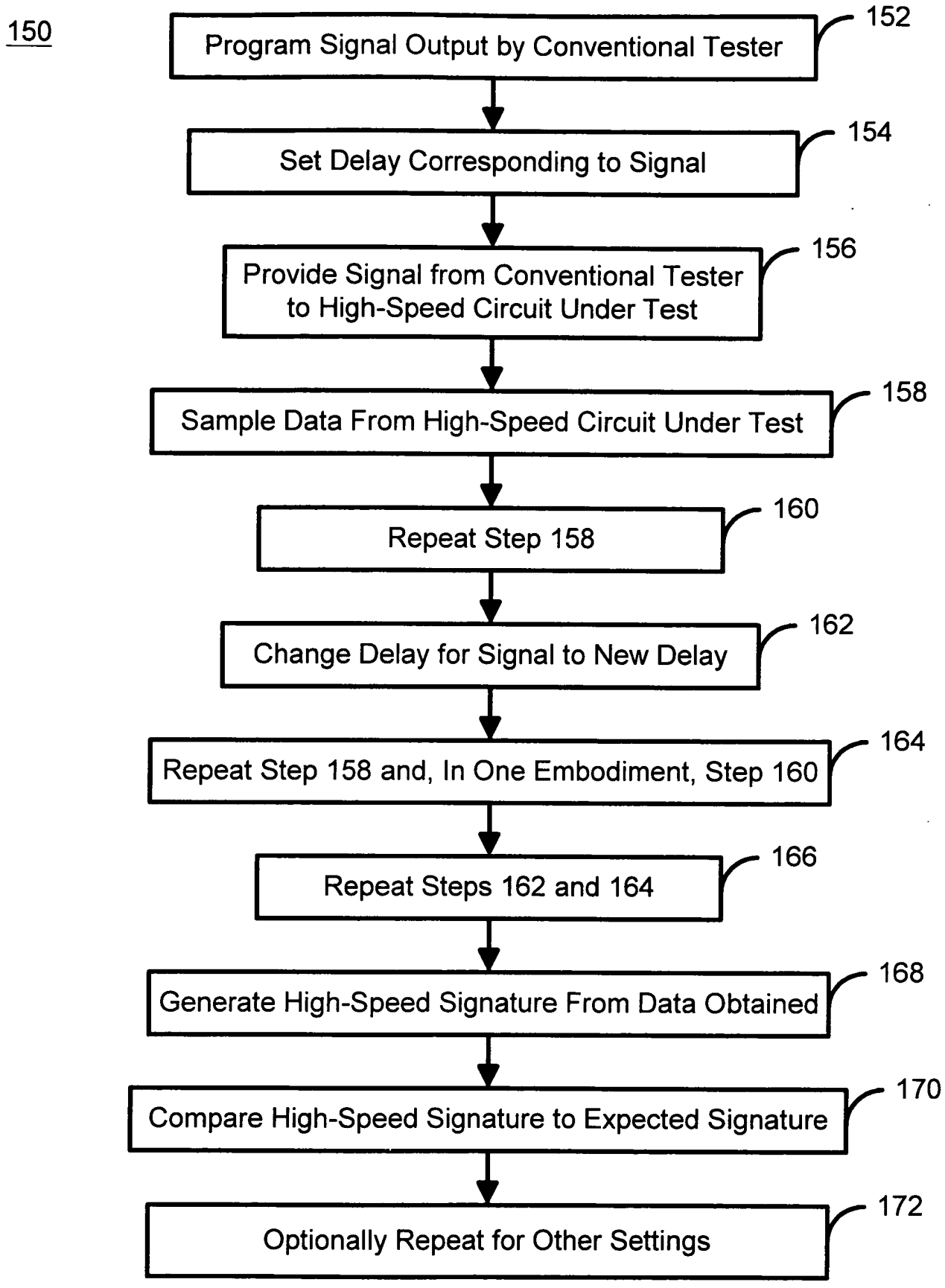
150
Program Signal Output by Conventional Tester
152
Set Delay Corresponding to Signal
154
Provide Signal from Conventional Tester to High-Speed Circuit Under Test
156
Sample Data From High-Speed Circuit Under Test
158
Repeat Step 158
160
Change Delay for Signal to New Delay
162
Repeat Step 158 and, In One Embodiment, Step 160
164
Repeat Steps 162 and 164
166
Generate High-Speed Signature From Data Obtained
168
Compare High-Speed Signature to Expected Signature
170
Optionally Repeat for Other Settings
172

Figure 4

400

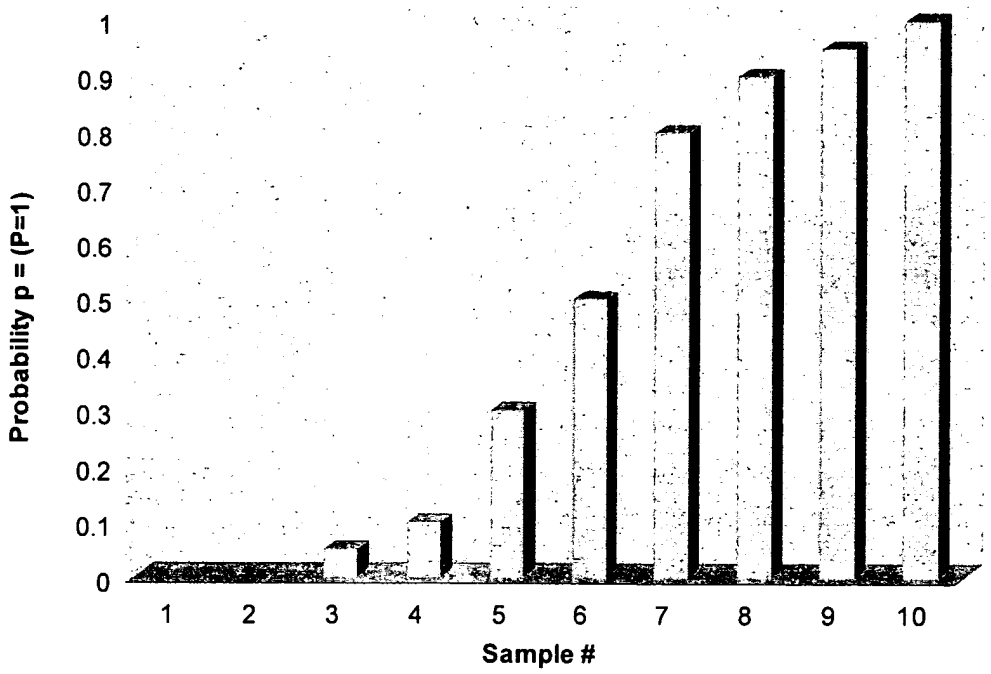

Figure 5

410

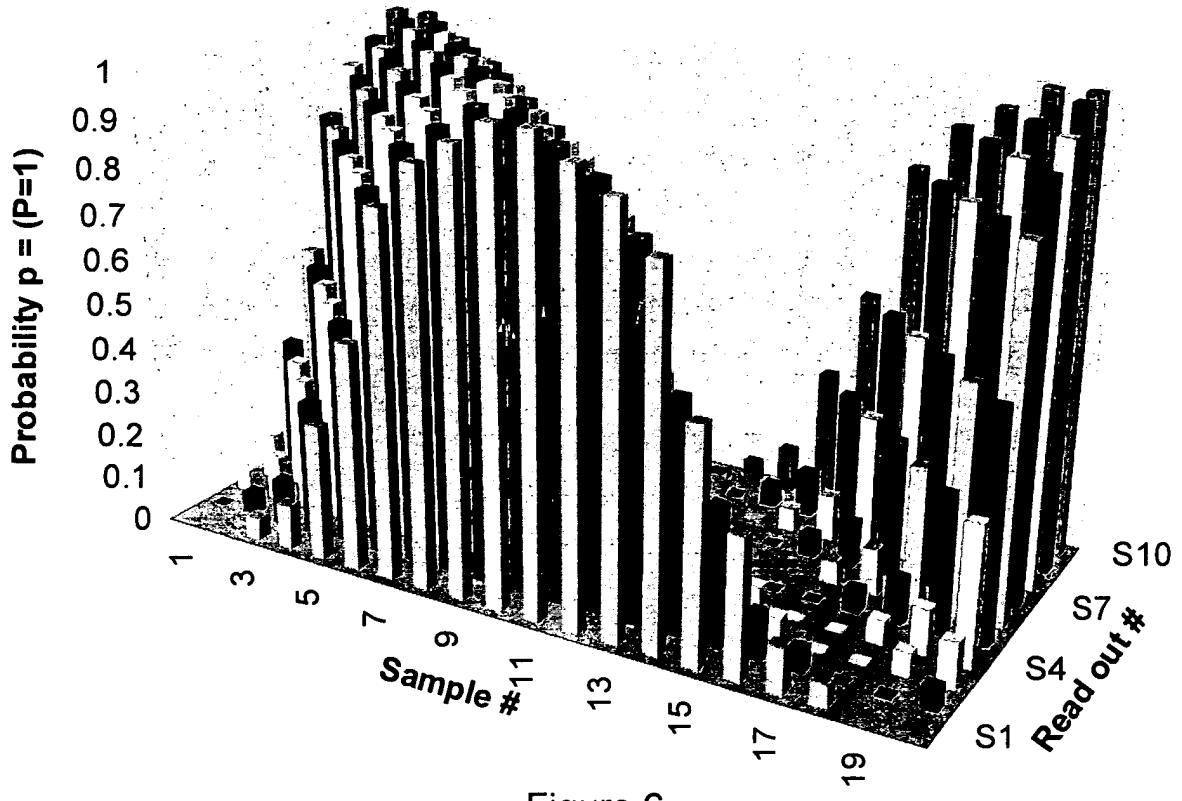

Figure 6

420

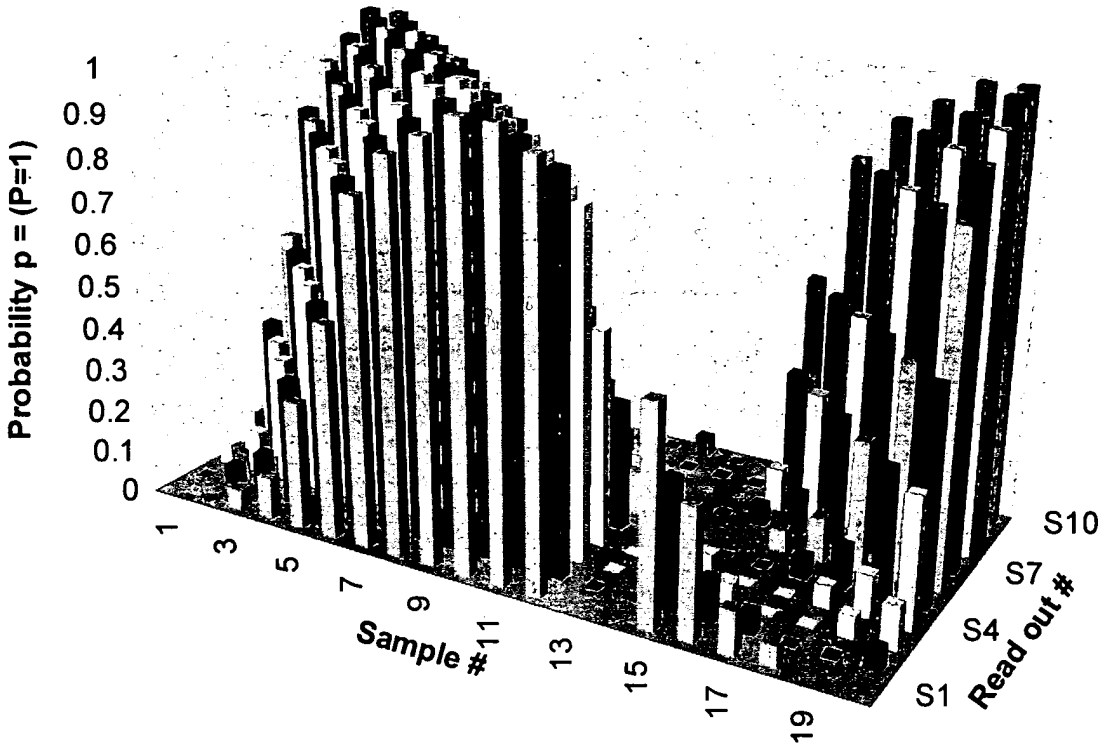

Figure 7

430

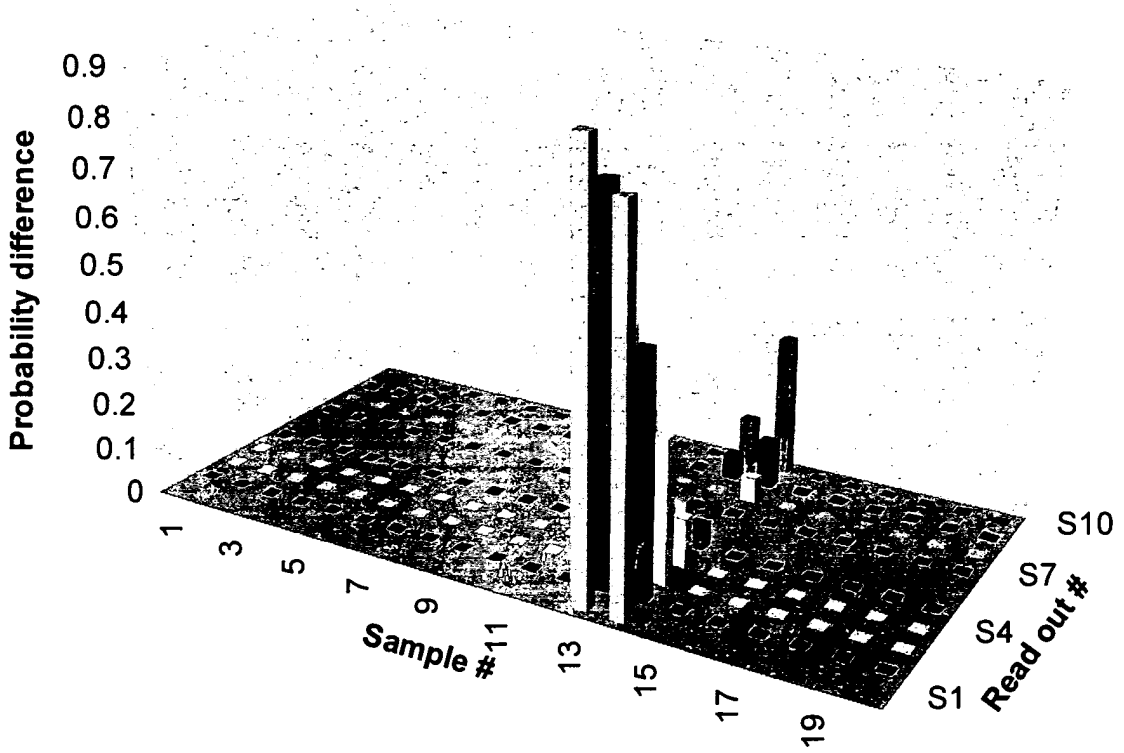

Figure 8